

REMARKS

Applicant respectfully requests reconsideration of the present U.S. patent application. Claim 14 has been rejected under 35 U.S.C. § 112, second paragraph. Claims 1-5, 7 and 11-20 stand rejected under 35 U.S.C. § 102. Claims 6 and 8-10 have been objected to, but would be allowable if rewritten in independent form. Claims 1, 2, 11-14 and 16-19 have been amended. Claim 20 has been canceled. Claim 21 has been added. Therefore, claims 1-19 and 21 are pending.

Claim Rejections - 35 U.S.C. § 112

Claim 14 was rejected under 35 U.S.C. § 112, second paragraph, because of the term “diode element.” Applicant has changed “diode element” to “diode” in claim 14. Applicant respectfully submits that the rejection has been overcome. Applicant therefore respectfully requests that the Examiner withdraw the rejection of claim 14 under 35 U.S.C. § 112, second paragraph.

Claim Rejections - 35 U.S.C. § 102

Rejections of Claims 1-5, 7 and 11-20 based on *Zametzer*

Claims 1-5, 7 and 11-20 were rejected under 35 U.S.C. § 102 as being anticipated by U.S. Patent No. 5,220,679 issued to *Zametzer* (*Zametzer*). Claim 20 has been canceled. Therefore, the rejection of claim 20 is moot. For at least the reasons set forth below, Applicant submits that claims 1-5, 7 and 11-19 are not anticipated by *Zametzer*.

Claim 1 recites the following:

a boost circuit configured to receive the RF input signal and the first DC supply voltage and in response, provide a DC output voltage that allows the RF input signal to be routed through the switch element, wherein the DC output voltage is greater than the first DC supply voltage.

Claim 16 recites a similar limitation.

Zametzer discloses a transmitting-receiving diplexer that includes an RF switch in a receiving channel, and a rectifier. See Abstract; col. 4, lines 28-32. In a transmit mode, the rectifier receives a transmission signal and supplies a blocking voltage to the RF switch, so that the transmission signal does not flow through the RF switch. See col. 3, lines 51-55; col. 5, lines 57-64. *Zametzer* does not disclose a boost circuit configured to receive an RF input signal and the first DC supply voltage and in response, provide a DC output voltage that allows the RF input signal to be routed through the switch element, wherein the DC output voltage is greater than the first DC supply voltage. Thus, *Zametzer* fails to disclose at least one limitation of claims 1 and 16. Consequently, claims 1 and 16 are not anticipated by *Zametzer*. Applicant therefore respectfully requests that the Examiner withdraw the rejections of claims 1 and 16 under 35 U.S.C. § 102.

Claims 2-5, 7 and 11-15 depend from claim 1. Claims 17-19 depend from claim 16. Because dependent claims include the limitations of the claims from which they depend, Applicant submits that claims 2-5, 7, 11-15 and 17-19 are not anticipated by *Zametzer* for at least the reasons set forth above. Applicant therefore respectfully requests that the Examiner withdraw the rejections of claims 2-5, 7, 11-15 and 17-19 under 35 U.S.C. § 102.

Rejections of Claims 1-3, 11 and 16 based on *Miyatsuji*

Claims 1-3, 11 and 16 were rejected under 35 U.S.C. § 102 as being anticipated by “A GaAs High-Power RF Single Pole Double-Throw Switch IC for Digital Mobile

Communication System” by Miyatsuji (*Miyatsuji*). For at least the reasons set forth below, Applicant submits that claims 1-3, 11 and 16 are not anticipated by *Miyatsuji*.

Claim 1 recites the following:

a boost circuit configured to receive the RF input signal and the first DC supply voltage and in response, provide a DC output voltage that allows the RF input signal to be routed through the switch element, wherein the DC output voltage is greater than the first DC supply voltage.

Claim 16 recites similar limitations.

Miyatsuji discloses a switching unit that includes FET 1, which receives an RF input signal and a control voltage A. See Fig. 2; paragraph 5. When FET 1 receives control voltage A, FET 1 is short-circuited, and the RF input signal is not transferred through the switch. See paragraph 5. *Miyatsuji* does not disclose a boost circuit configured to receive an RF input signal and the first DC supply voltage and in response, provide a DC output voltage that allows the RF input signal to be routed through the switch element, wherein the DC output voltage is greater than the first DC supply voltage. Thus, *Miyatsuji* fails to disclose at least one limitation of claims 1 and 16. Consequently, claims 1 and 16 are not anticipated by *Miyatsuji*. Applicant therefore respectfully requests that the Examiner withdraw the rejections of claims 1 and 16 under 35 U.S.C. § 102.

Claims 2, 3 and 11 depend from claim 1. Because dependent claims include the limitations of the claims from which they depend, Applicant submits that claims 2, 3 and 11 are not anticipated by *Miyatsuji* for at least the reasons set forth above. Applicant therefore respectfully requests that the Examiner withdraw the rejections of claims 2, 3 and 11 under 35 U.S.C. § 102.

Allowable Subject Matter

Claims 6 and 8-10 were objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. As explained above, claim 1 is not anticipated by the cited references. Consequently, claim 1 is in condition for allowance.

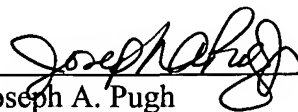
Claims 6 and 8-10 depend from claim 1. Dependent claims include the limitations of the claims from which they depend. Therefore, Applicant submits that although not rewritten in independent form to include all of the limitations of the base claim and any intervening claims, claims 6 and 8-10 are in condition for allowance.

CONCLUSION

For at least the foregoing reasons, Applicant submits that the rejections have been overcome. Therefore, claims 1-19 and 21 are in condition for allowance and such action is respectfully solicited. The Examiner is respectfully requested to contact the undersigned by telephone if such contact would further the examination of the present application.

Respectfully submitted,

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